FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. IMEC218.001AUS	APPLICATION NO. 09/935,789
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT (USE SEVERAL SHEETS IF NECESSARY)	APPLICANT Catthoor, et al.	
	FILING DATE August 22, 2001	GROUP 2151

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
	1	5,202,975	04/13/93	Rasbold et al.			
	2	5,327,561	07/05/94	Choi et al.			
	3	5,594,864	01/1997	Trauben	714	39	
	4	5,664,193	09/1997	Tirumalai	717	153	
	5	5,742,814	04/21/98	Balasa et al.			
	6	5,930,510	07/27/99	Beylin et al.			
	7	5,978,509	11/1999	Nachtergaele et al.	382	236	
	8	6,064,819	05/2000	Franssen et al.	717	156	
	9	6,078,745	06/2000	De Greef et al.	717	151	
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	11	Al-Furiah et al., "Memory Hierarchy Management for Interactive graph Structures", Proceedings, IEEE International Symposium on Parallel and Distributed Processing, March-April, 1998.
	12	Al-Mouhamed, M., et al., "A Heuristic Storage for Minimizing Access Time of Arbitrary Data Patterns", *IEEE Transactions on Parallel and Distributed Systems*, Vol. 8 No. 4, April 1997.
	13	Balasa, F., et al., "Dataflow-Driven Memory Allocation for Multi-Dimensional Signal Processing Systems", *Proceedings IEEE International Conference on Computer Aided Design, San Jose, CA*, pps. 31-34, Nov. 1994.
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	19	De Greef, E., et al. "Mapping Real-Time Motion Estimation Type Algorithms to Memory Efficient, Programmable Multi-Processor Architectures, "Microprocessing and Microprogramming, 41(5): 409-423, October 1995.
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EXAMINER	DATE CONSIDERED 15-SEPTEMBER-2004

*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED. INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.

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	22	Fang, J., et al., "An Iteration Partition Approach for Cache or Local Memory Thrashing on Parallel Processing", IEEE Transactions on Computers, Vol. C-42, No 5, pp. 529-546, May 1993.
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	25	Lippens, P., et al, "Allocation of Multiport Memories for Hierarchical Data Streams", *Proceedings IEEE International Conference on Computer Aided Designs*, pps. 728-735, Santa Clara, Nov. 1993.
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EXAMINER	DATE CONSIDERED 15-SEPTEMBER 2004

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